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• Yanai, Tsuyoshi, c/o Fujitsu LSI Technology Ltd.
Kanagawa 213-0012 (JP)
• Ohkawa, Takashi,
c/o Fujitsu LSI Technology Ltd.
Kanagawa 213-0012 (JP)

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(71) Applicant: **FUJITSU LIMITED**
Kawasaki-shi, Kanagawa 211-8588 (JP)

(74) Representative: **Stebbing, Timothy Charles et al**
Haseltine Lake & Co.,
Imperial House,
15-19 Kingsway
London WC2B 6UD (GB)

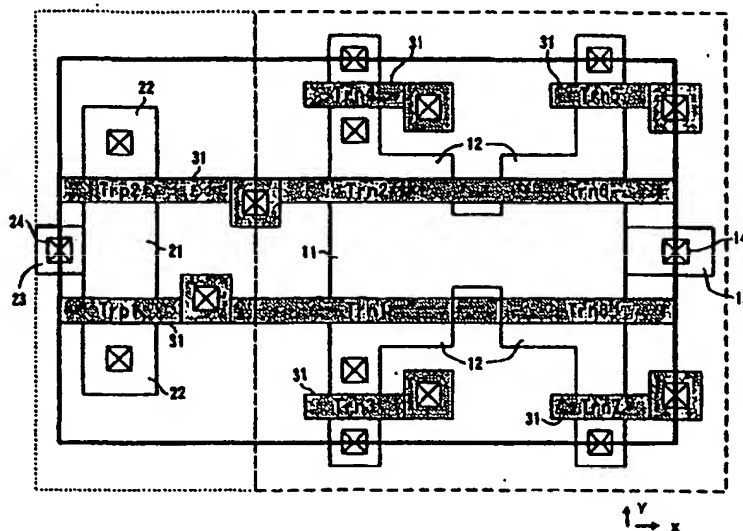
(72) Inventors:
• Kajii, Yoshio, c/o Fujitsu Limited
Kawasaki-shi, Kanagawa 211-8588 (JP)

(54) **Semiconductor memory device**

(57) A semiconductor memory device is constructed to include a memory cell formed by a plurality of transis-

tors, wherein each of gate wiring layers of all of the transistors forming the memory cell is arranged to extend in one direction.

FIG.4



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EUROPEAN SEARCH REPORT

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Place of search	Date of completion of the search	Examiner	
Munich	29 November 2004	Blackley, W	
CATEGORY OF CITED DOCUMENTS		T: theory or principle underlying the invention E: earlier patent document, but published on, or after the filing date D: document cited in the application L: document cited for other reasons &: member of the same patent family, corresponding document	
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